

### General Description

The WSD100N06GDN56 is the SGT MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD100N06GDN56 meet the RoHS and Green Product requirement 100%  $E_{AS}$  guaranteed with full function reliability approved.

### Features

- Lead Free and Green Devices Available (RoHS Compliant)
- 100% UIS + Rg Tested
- Reliable and Rugged
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

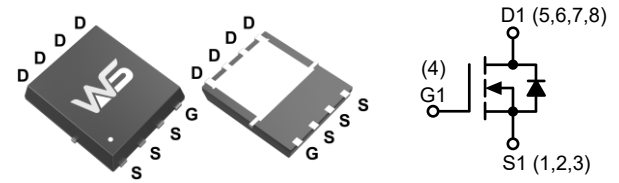
### Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
60V	3.0m $\Omega$	100A

### Applications

- Secondary Side Synchronous Rectification
- DC-DC Converter
- Motor Control
- Load Switching

### DFN5X6-8L Pin Configuration



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	60	V	
$V_{GS}$	Gate-Source Voltage	$\pm 20$		
$I_D^{1,6}$	Continuous Drain Current	$T_C=25^\circ\text{C}$	100	A
		$T_C=100^\circ\text{C}$	65	
$I_{DM}^2$	Pulsed Drain Current	$T_C=25^\circ\text{C}$	240	W
$P_D$	Maximum Power Dissipation	$T_C=100^\circ\text{C}$	50	
$I_{AS}$	Avalanche Current, Single pulse	45	A	
$E_{AS}^3$	Single Pulse Avalanche Energy	101	mJ	
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
$T_J$	Operating Junction Temperature Range	150		

### Thermal Data

Symbol	Parameter	Rating	Units
$R_{\theta JA}^1$	Thermal Resistance Junction to ambient	Steady State	$^\circ\text{C}/\text{W}$
$R_{\theta JC}^1$	Thermal Resistance-Junction to Case	Steady State	

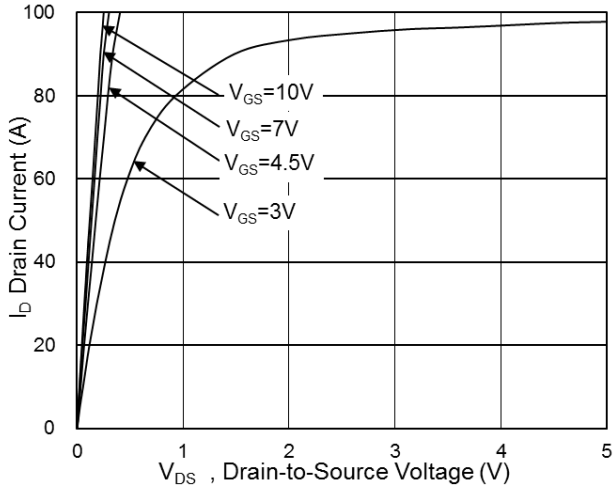
**Electrical Characteristics (T<sub>A</sub>=25°C, Unless Otherwise Noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
<b>Static</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60	---	---	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	---	---	1.0	μA
		T <sub>J</sub> =85°C	---	---	30	
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	1.2	1.8	2.5	V
R <sub>DS(ON)</sub> <sup>2</sup>	Drain-Source On-state Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	3.0	3.6	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	---	4.4	5.4	
<b>Switching</b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	58	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	16	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	4.0	---	
T <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GEN</sub> =10V, V <sub>DD</sub> =30V, I <sub>D</sub> =20A, R <sub>G</sub> =3Ω	---	18	---	ns
T <sub>r</sub>	Turn-on Rise Time		---	8	---	
T <sub>d(off)</sub>	Turn-off Delay Time		---	50	---	
T <sub>f</sub>	Turn-on Fall Time		---	11	---	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz	---	0.7	---	Ω
<b>Dynamic</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =30V, f=1.0MHz	---	3458	---	pF
C <sub>oss</sub>	Output Capacitance		---	1522	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	22	---	
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub> <sup>1,5</sup>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	55	A
I <sub>SM</sub>	Pulsed Source Current		---	---	240	
V <sub>SD</sub> <sup>2</sup>	Diode Forward Voltage	I <sub>SD</sub> =1A, V <sub>GS</sub> =0V	---	0.8	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> =20A, dI <sub>SD</sub> /dt=100A/μs	---	27	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge		---	33	---	nC

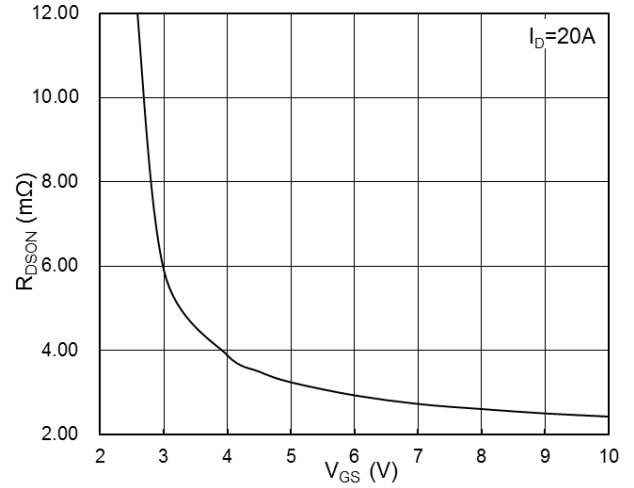
**Note:**

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper, t≤10sec.
- The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.
- The E<sub>AS</sub> data shows Max. rating. The test condition is V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.5mH, I<sub>AS</sub>=40A
- The power dissipation is limited by 150°C junction temperature.
- The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.
- The maximum current rating is package limited.

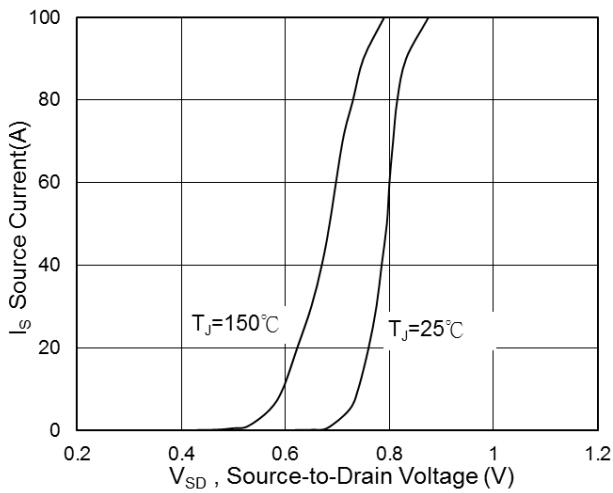
**Typical Characteristics**



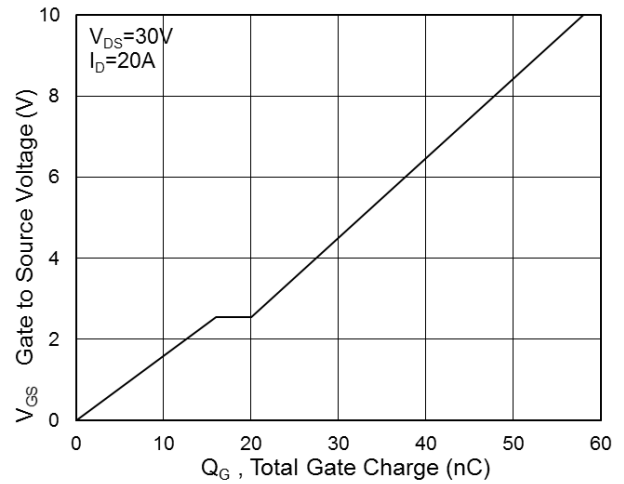
**Fig.1 Typical Output Characteristics**



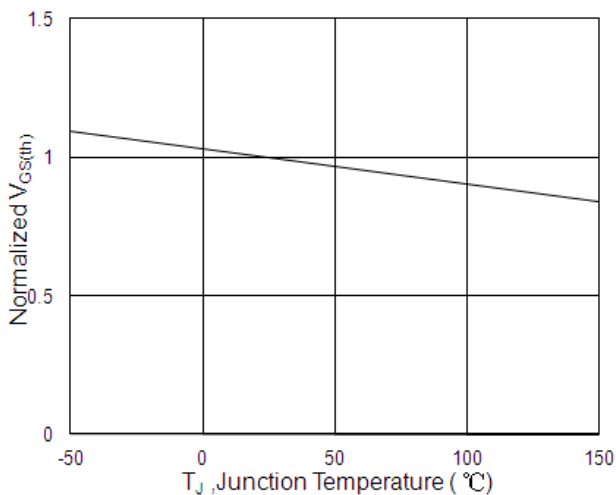
**Fig.2 On-Resistance vs G-S Voltage**



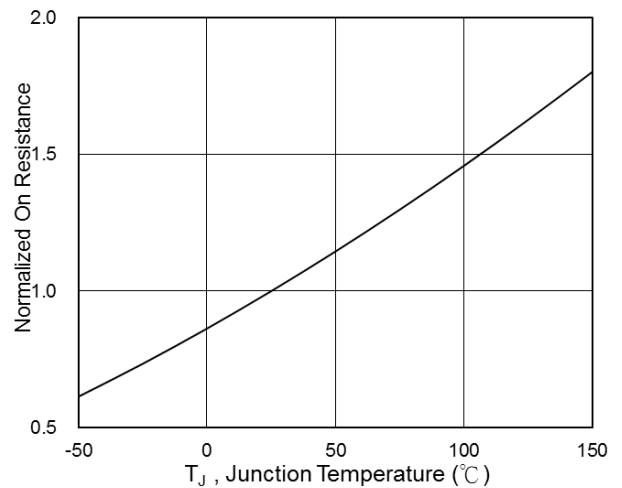
**Fig.3 Diode Forward Voltage vs. Current**



**Fig.4 Gate-Charge Characteristics**

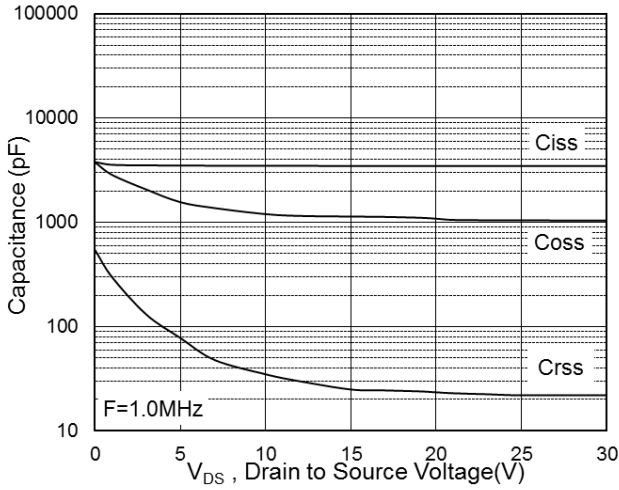


**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**

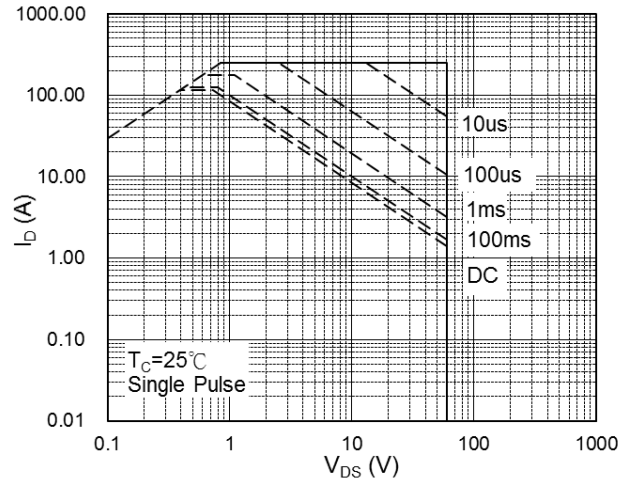


**Fig.6 Normalized  $R_{DSON}$  vs  $T_J$**

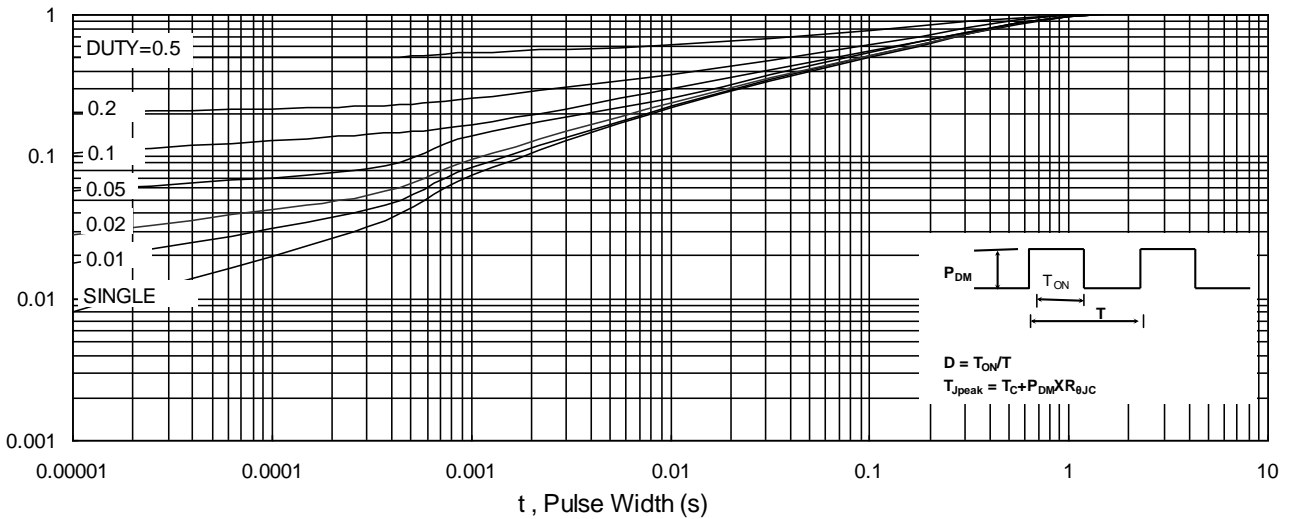
**Typical Characteristics (Cont.)**



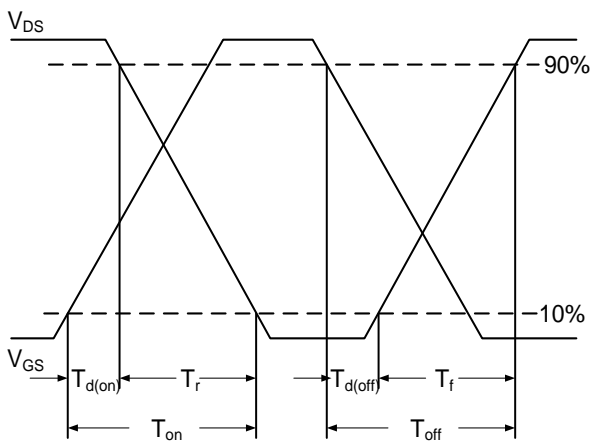
**Fig.7 Capacitance**



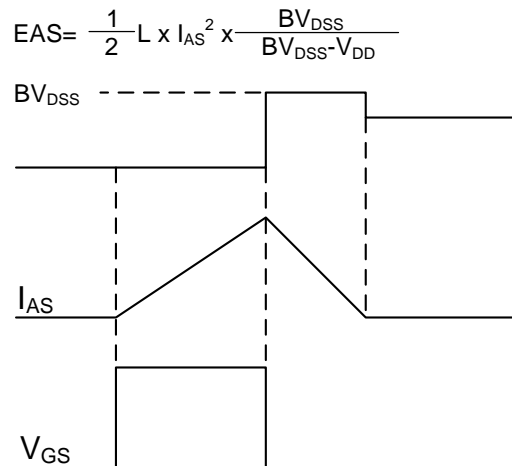
**Fig.8 Safe Operating Area**



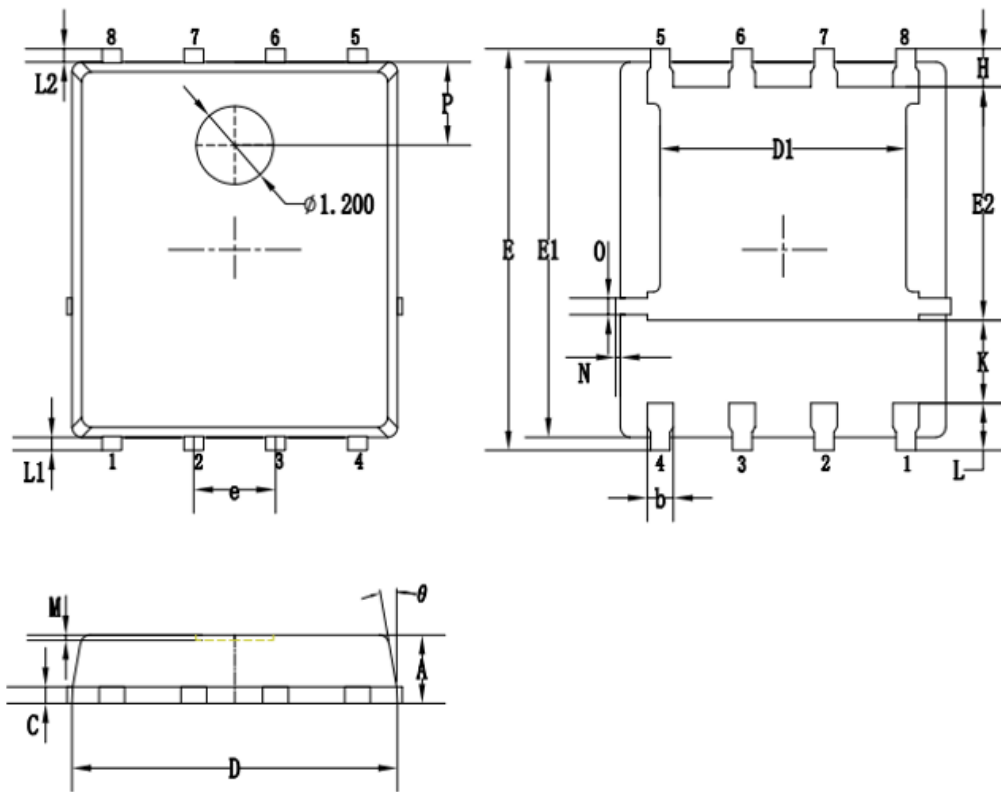
**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

**Packaging information**


SYMBOLS	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.20
D1	3.72	3.82	3.92
E	6.00	6.15	6.30
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.74	0.84
L1/L2	0.20 REF.		
$\theta$	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

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